

**Coelho, Diego F. G.; Cintra, Renato J.; Rajapaksha, Nilanka; Mendis, Gihan J.; Madanayake, Arjuna; Dimitrov, Vassil S.**

**DFT computation using Gauss-Eisenstein basis: FFT algorithms and VLSI architectures.**

(English) [Zbl 1374.94766](#)

IEEE Trans. Comput. 66, No. 8, 1442-1448 (2017).

Editorial remark: No review copy delivered.

**MSC:**

**94A55** Shift register sequences and sequences over finite alphabets in information and communication theory

**65T50** Numerical methods for discrete and fast Fourier transforms

**68W35** Hardware implementations of nonnumerical algorithms (VLSI algorithms, etc.)

**Full Text:** [DOI](#)