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Configuration of VLSI arrays in the presence of defects. (English) Zbl 0632.94033

J. Assoc. Comput. Mach. 31, 694-717 (1984).

MSC:

94C15 Applications of graph theory to circuits and networks

Cited in **6** Documents

94C10 Switching theory, application of Boolean algebra; Boolean functions (MSC2010)

68M20 Performance evaluation, queueing, and scheduling in the context of computer systems

Keywords:

VLSI arrays; circuit area; fault tolerance; percolation theory; probabilistic analysis; queueing processes; systolic arrays; wafer-scale integration; wire length

Full Text: [DOI](#)