

Nath, Dhruva; Maheshwari, S. N.; Bhatt, P. C. P.

Efficient VLSI networks for parallel processing based on orthogonal trees. (English)

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IEEE Trans. Comput. 32, 569-581 (1983).

For a scan of this review see the [web version](#).

MSC:

68N25 Theory of operating systems

68R10 Graph theory (including graph drawing) in computer science

Cited in **12** Documents

Keywords:

area-time complexity; interconnection networks; matrix multiplication; orthogonal trees networks; parallel algorithms; parallel processing; sorting

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